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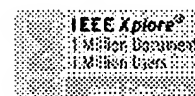If you use the OR operation, please leave a **SPACE** between keywords.

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1 **Using insertion convolutional code for speech transmission over GERAN 8PSK voice bearers**
Wen Xu; Marke, M.;
Personal, Indoor and Mobile Radio Communications, 2001 12th IEEE International Symposium on , Volume: 1 , 30 Sept.-3 Oct. 2001
Pages:D-119 - D-123 vol.1

[Abstract]   [PDF Full-Text (399 KB)]   **IEEE CNF**

2 **A class of multirate convolutional codes by dummy bit insertion**
Xu, W.; Romme, J.;
Global Telecommunications Conference, 2000. GLOBECOM '00. IEEE , Volume: 2 , 27 Nov.-1 Dec. 2000
Pages:830 - 834 vol.2

[Abstract]   [PDF Full-Text (368 KB)]   **IEEE CNF**

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1 **Low-power charge-sharing ROM using dummy bit lines**
Byung-Do Yang; Lee-Sup Kim;
Electronics Letters , Volume: 39 , Issue: 14 , 10 July 2003
Pages:1041 - 1042

[Abstract]   [PDF Full-Text (251 KB)]   **IEE JNL**

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2 **A class of multirate convolutional codes by dummy bit insertion**
Xu, W.; Romme, J.;
Global Telecommunications Conference, 2000. GLOBECOM '00. IEEE , Volume: 2 , 27 Nov.-1 Dec. 2000
Pages:830 - 834 vol.2

[Abstract]   [PDF Full-Text (368 KB)]   **IEEE CNF**

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3 **A low power charge sharing ROM using dummy bit lines**
Byung-Do Yang; Lee-Sup Kim;
Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on , Volume: 5 , 25-28 May 2003
Pages:V-377 - V-380 vol.5

[Abstract]   [PDF Full-Text (357 KB)]   **IEEE CNF**

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4 **Advanced DC-free track code pattern using diphase code for perpendicular recording**
Hamaguchi, T.; Maeda, H.; Shishida, K.;
Magnetics, IEEE Transactions on , Volume: 41 , Issue: 1 , Jan. 2005
Pages:137 - 139

[Abstract]   [PDF Full-Text (144 KB)]   **IEEE JNL**

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5 **A 68-ns 4-Mbit CMOS EPROM with high-noise-immunity design**
Imamiya, K.; Miyamoto, J.; Atsumi, S.; Ohtsuka, N.; Muroya, Y.; Sako, T.; Higashino, M.; Iyama, Y.; Mori, S.; Ohshima, Y.; Araki, H.; Kaneko, Y.; Narita, K.; Arai, N.; Yoshikawa, K.; Tanaka, S.;
Solid-State Circuits, IEEE Journal of , Volume: 25 , Issue: 1 , Feb. 1990
Pages:72 - 78

[Abstract]   [PDF Full-Text (652 KB)]   **IEEE JNL**

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6 **Design and analysis of a high-speed sense amplifier for**

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